Docket No.

252311US2S

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Syotaro ONO, et al.

SERIAL NO: New Application

GAU:

FILED:

Herewith

EXAMINER:

FOR:

Herewith EXAMINERS

SEMICONDUCTOR DEVICE HAVING VERTICAL MOS GATE STRUCTURE AND METHOD OF MANUFACTURING THE SAME

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement
- □ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number <u>15-0030</u>. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Marvih J. Spivak

Registration No. 24,913

James D. Hamilton
Registration No. 28,421

Customer Number

22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03) DOCKET NO.: 252311US2S

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FOR: SEMICONDUCTOR DEVICE HAVING VERTICAL MOS GATE

STRUCTURE AND METHOD OF MANUFACTURING THE SAME

STATEMENT OF RELEVANCY

Reference AO (5-7002) of Form PTO-1449:

This reference shows a similar cross-sectional view of a Split-Gate structure for power MOSFET.

Reference AP (9-181311) of Form PTO-1449:

Figure 1 shows similar cross-sectional view of a split-Gate structure.

Form PTO 1449		U.S. DEPARTMENT	OF COMMERCE	ATTY DOCKET NO. 252311US2S		SERIAL NO. New Application		
(Modified)		PATENT AND TRAI	DEMARK OFFICE					
				APPLICANT				
LIST OF	REFEF	RENCES CITED BY AP	PLICANT	Syotaro ONO, et al.				
				FILING DATE		GROUP		
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U.S. PATENT DOCUMENTS								
EXAMINER		DOCUMENT	DATE	NAME	CLASS SUB FILING DATE			
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FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY		YES		LATION NO
	AO	5-7002	01/14/93	Japan				Х
	AP	9-181311	07/11/97	Japan				X
	AQ	2002-94061	03/29/02	Japan (with English abstract)				X
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)								
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Examiner			Date Considered					
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in								
conformance and not considered. Include copy of this form with next communication to applicant.								